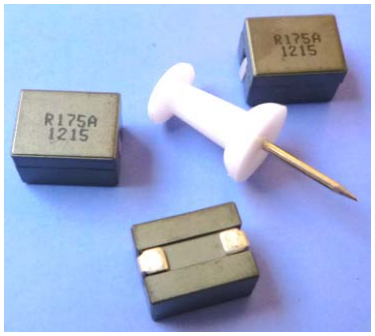
SL4130 Series

1. Features:

· Ferrite based SMD Inductor with lower core loss.
· Inductance Range:120nH to 300nH. Custom values are welcomed.
· High current output chokes, upto 94 Amp with approx. 20% roll off.
· Low Profile 7.5mm Max. height .
· Foot Print 10.41 x 8.0 mm Max.
· Ideal for Buck Converter, VRM & High Density Board Design.
· Operating frequency up to 1 MHz application.
· Operating Temperature Range -55°C to + 130°C , RoHs & HF compliance .
· T & R Qtys: 750 pcs , 13" Reel ;

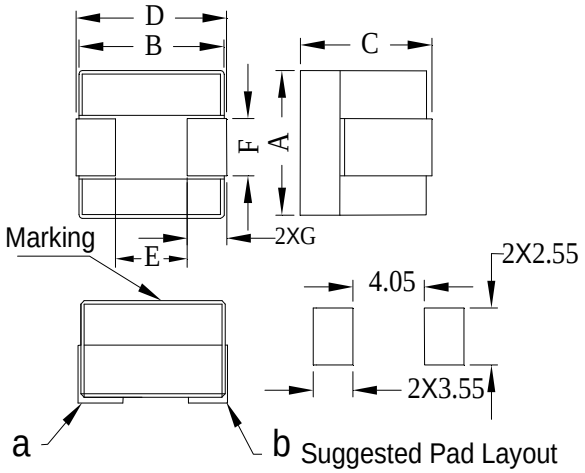


2. Electrical Characteristic of SL4130 Series:

Part Number	Inductance (uH) ±10% or 15%	DCR (mΩ) ±5.0%	Isat[1] (A) @25℃	Isat[2] (A) @45℃	Isat[3] (A) @100℃	Irms[4] (A) @25℃
SL4130A-R12KHF	0.120 , 10%	0.29	94.0	90.0	76.0	61.0
SL4130B-R12KHF	0.120 , 10%	0.27	94.0	90.0	76.0	63.0
SL4130A-R15KHF	0.150 , 10%	0.29	80.0	76.0	67.0	61.0
SL4130B-R15KHF	0.150 , 10%	0.27	80.0	76.0	67.0	63.0
SL4130AH-R15KHF	0.150 , 10%	0.29	78.0	77.0	73.0	61.0
SL4130BH-R15KHF	0.150 , 10%	0.27	78.0	77.0	73.0	63.0
SL4130A-R17KHF	0.170 , 10%	0.29	72.0	69.0	60.0	61.0
SL4130B-R17KHF	0.170 , 10%	0.27	72.0	69.0	60.0	63.0
SL4130A-R175KHF	0.175 , 10%	0.29	68.0	65.0	56.0	61.0
SL4130B-R175KHF	0.175 , 10%	0.27	68.0	65.0	56.0	63.0
SL4130A-R20KHF	0.200 , 10%	0.29	62.0	59.0	52.0	61.0
SL4130B-R20KHF	0.200 , 10%	0.27	62.0	59.0	52.0	63.0
SL4130A-R21LHF	0.215 , 15%	0.29	55.0	52.0	50.0	61.0
SL4130B-R21LHF	0.215 , 15%	0.27	55.0	52.0	50.0	63.0
SL4130A-R23LHF	0.230 , 15%	0.29	54.0	50.0	42.0	61.0
SL4130B-R23LHF	0.230 , 15%	0.27	54.0	50.0	42.0	63.0
SL4130A-R26KHF	0.260 , 10%	0.29	47.0	45.0	38.0	61.0
SL4130B-R26KHF	0.260 , 10%	0.27	47.0	45.0	38.0	63.0
SL4130A-R27LHF	0.270 , 15%	0.29	42.0	39.0	35.0	61.0
SL4130B-R27LHF	0.270 , 15%	0.27	42.0	39.0	35.0	63.0
SL4130A-R30LHF	0.300 , 15%	0.29	39.0	38.0	32.0	61.0
SL4130B-R30LHF	0.300 , 15%	0.27	39.0	38.0	32.0	63.0

3. Mechanical Dimension(Unit:mm):

A Max.	B Max.	C Max.	D Max.	E Nom.	F Nom.	G Nom.
8.00	10.30	7.50	10.41	5.10	2.25	2.50

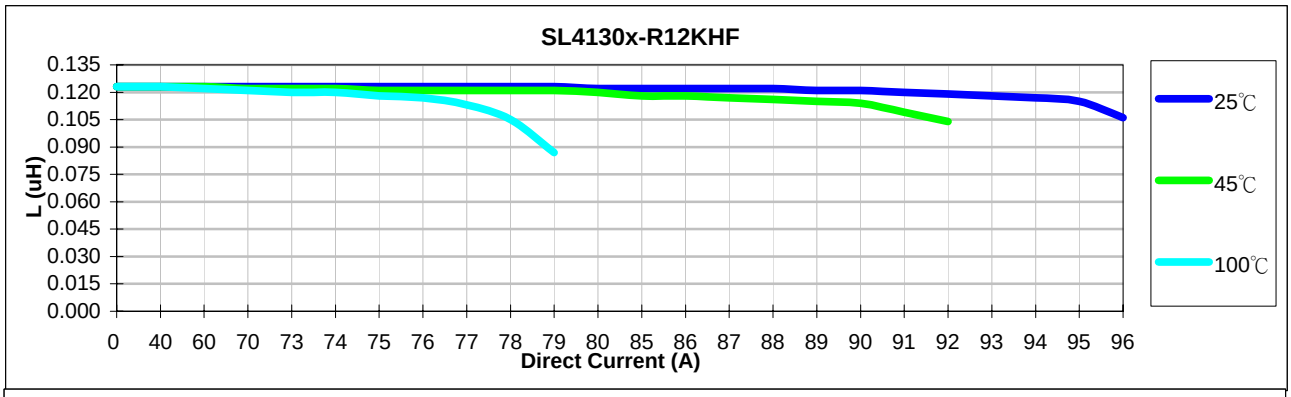


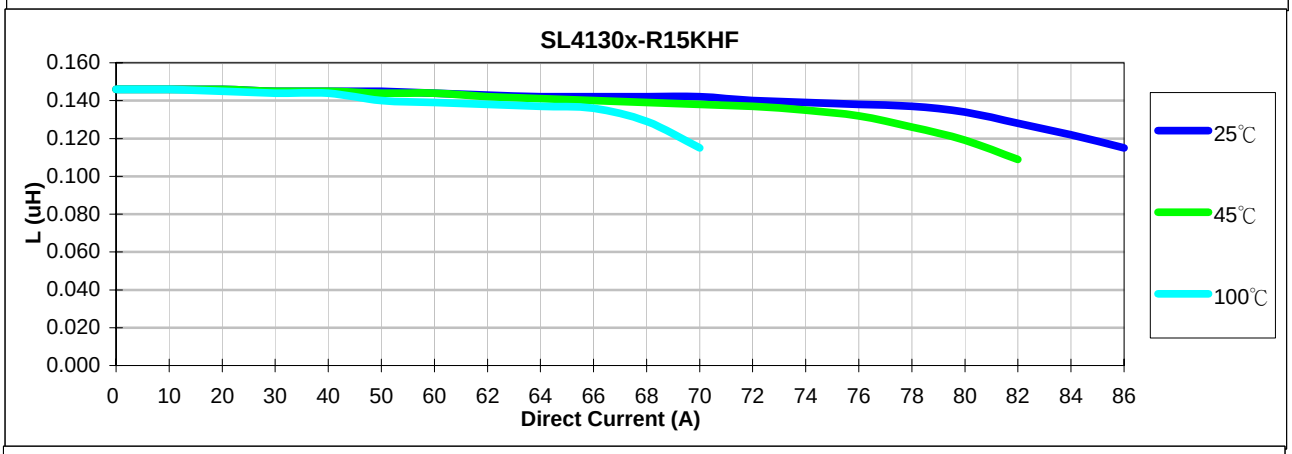
Note:

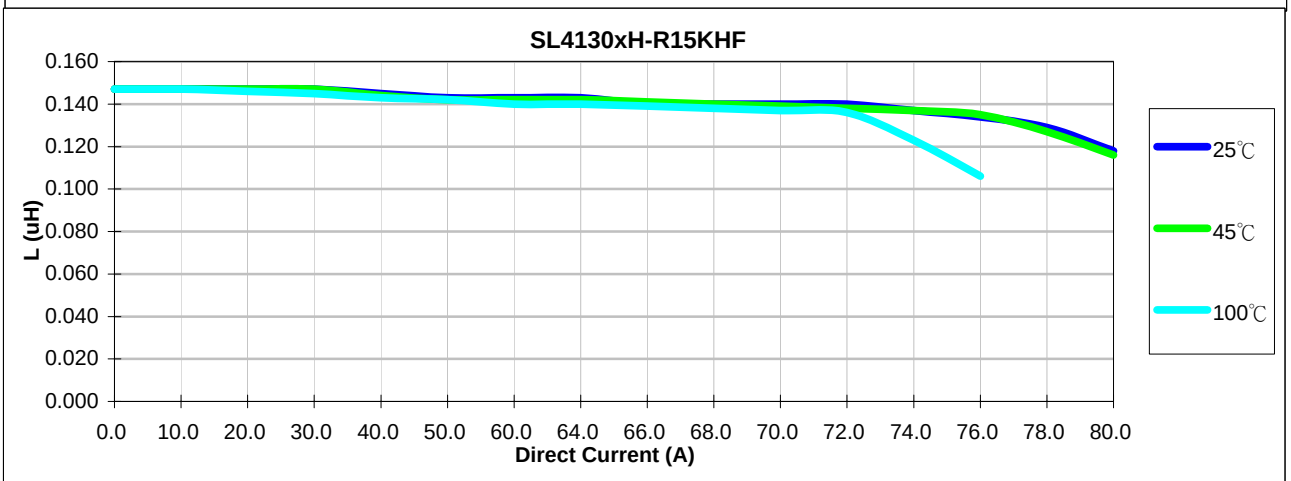
1>.Open Circuit Inductance (OCL) test condition:100KHz,0.1Vrms ,0Adc.
2>.Full Load Inductance (FLL) Test condition:100KHz,0.1Vrms ,Isat;(Ta=25 ℃).
3>.Isat[1],Isat[2] & Isat[3]: DC current that will cause inductance to drop approximately by 20%;(Ta=25 ℃).
4>. Irms: DC current for an approximate temperature rise of 40 ℃ without core loss,.Derating is necessary for AC currents. PCB pad layout,trace thickness and width,air-flow and proximity of other heat generating components will affect the temperature rise. It is recommended the part temperature not exceed 130°C under worst case operating conditions verified in the end application.
5>.The nominal DCR is measured from point "a" to point"b",as shown above on the mechanical drawing.

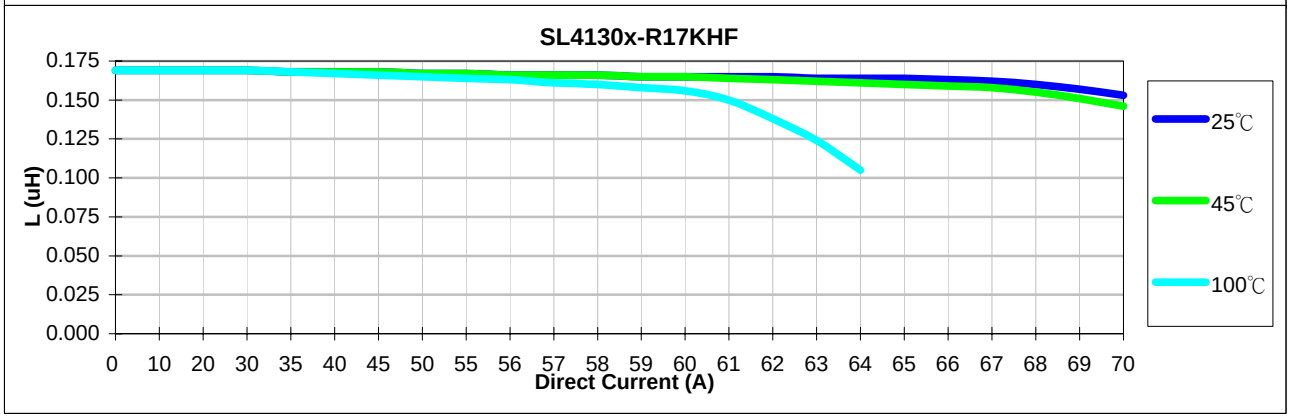
SL4130 Series

Inductance vs. Current









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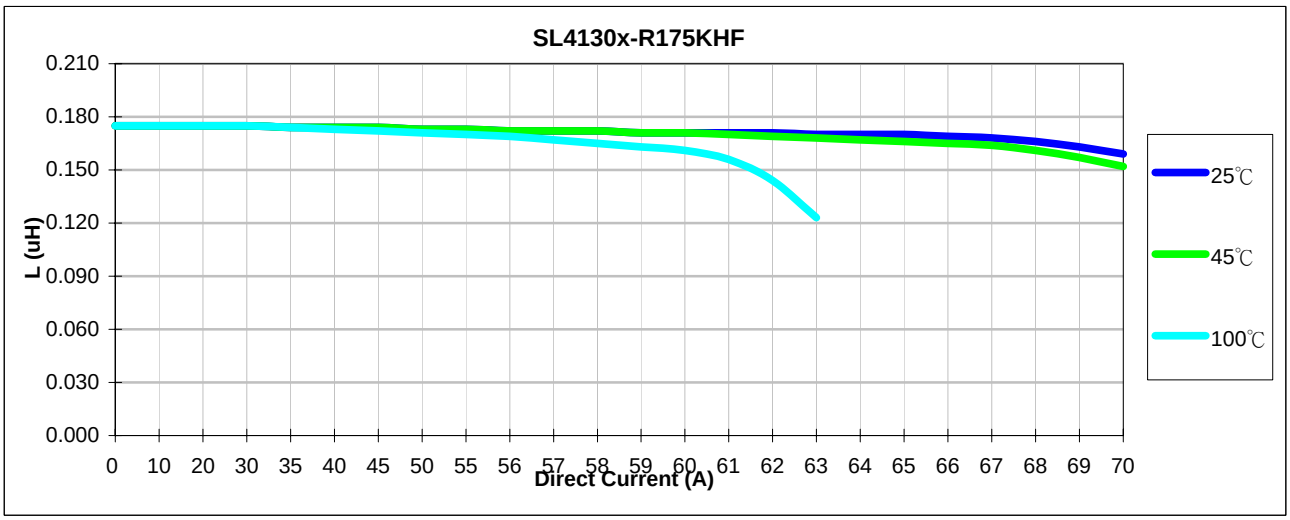
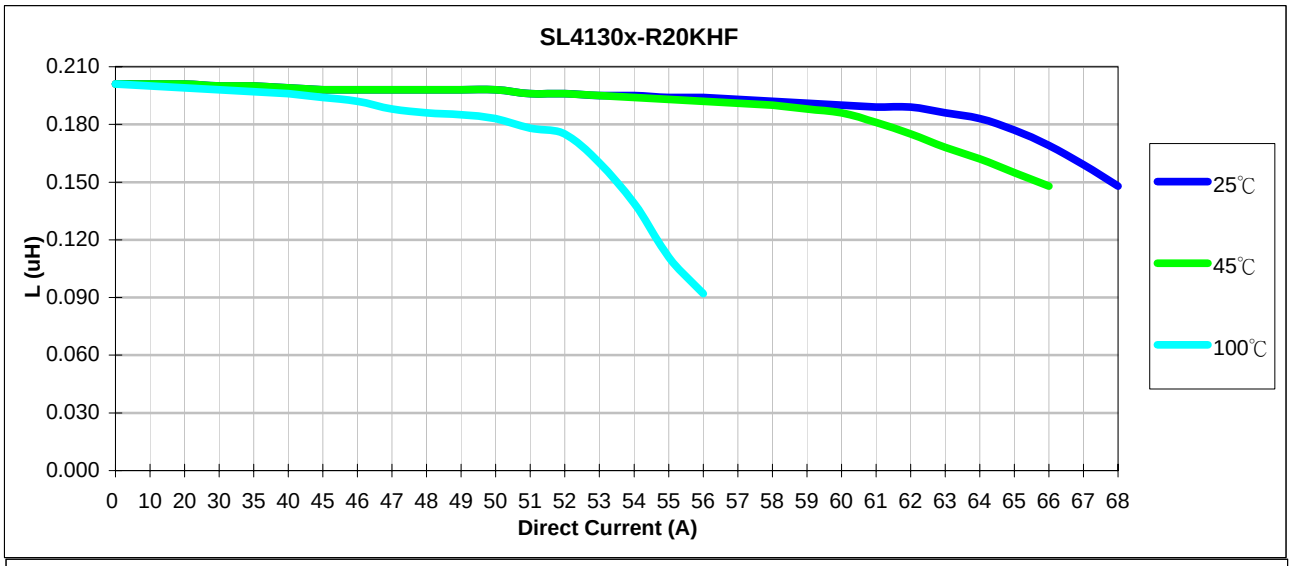
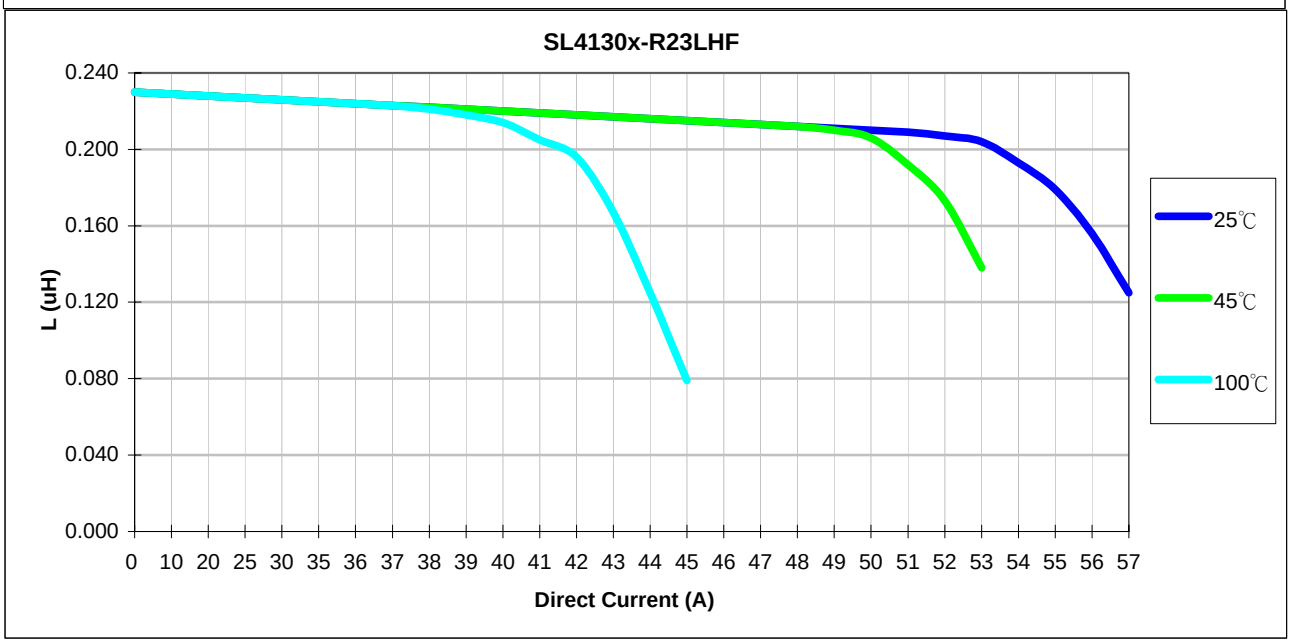
Inductance vs. Current







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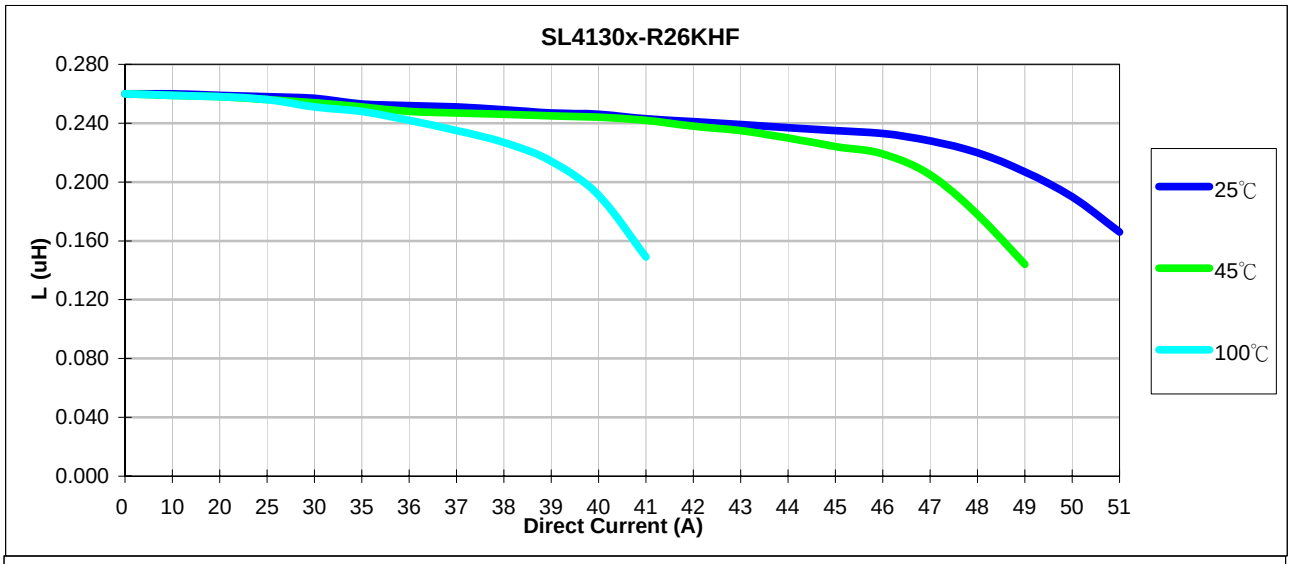
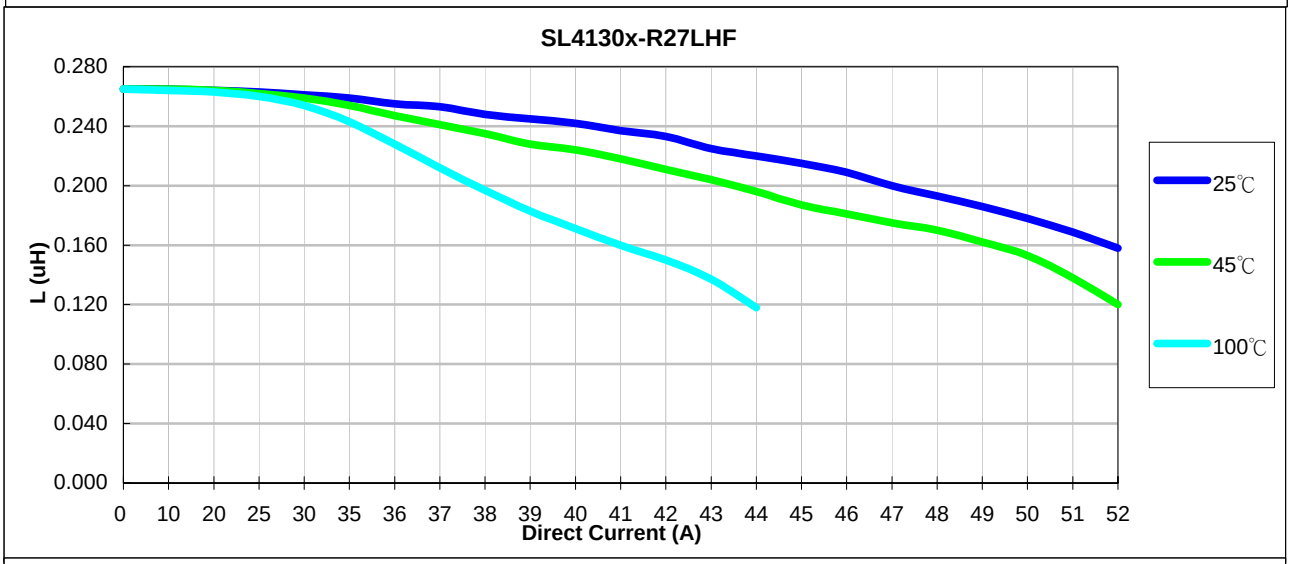
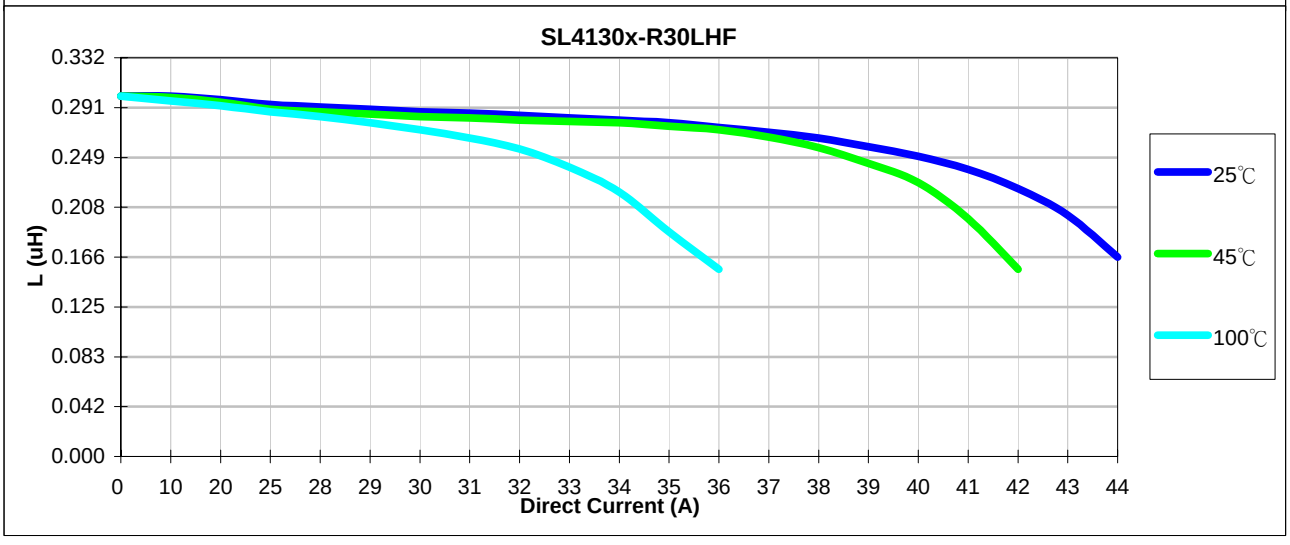
Inductance vs. Current







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