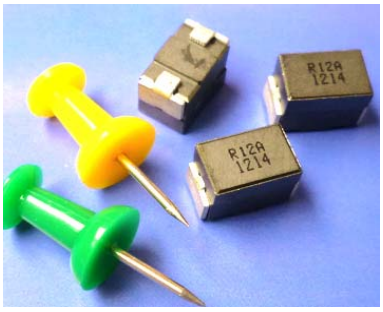
SL42326 Series

1. Features:



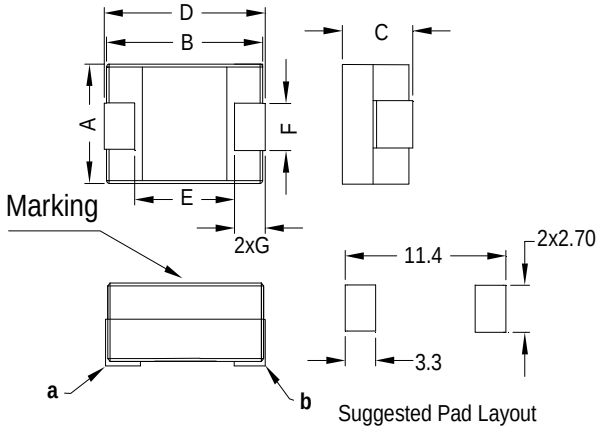
- Ferrite based SMD Inductor with lower core loss.
- Inductance Range:100nH to 180nH,Custom values are welcomed.
- High current output chokes, upto 90 Amp with approx. 20% roll off.
- Low Profile 8.1mm Max. height .
- Foot Print 10.8 x 6.3 mm Max.
- Ideal for Buck Converter, VRM & High Density Board Design.
- Operating frequency up to 1 MHz application.
- Operating Temperature Range -55°C to + 130°C , RoHs & HF compliance .

2. Electrical Characteristic of SL42326 Series:

Part Number	Inductance (uH) ±10%	DCR (mΩ) ± 5.0%	Isat[1] (A) @25℃	Isat[2] (A) @75℃	Isat[3] (A) @100℃	Irms (A) @25℃	D (mm) Max.
SL42326A-R10KHF	0.10	0.22	90.00	85.00	78.00	58.00	10.80
SL42326B-R10KHF	0.10	0.20	90.00	85.00	78.00	61.00	10.90
SL42326A-R12KHF	0.12	0.22	76.00	74.00	68.00	58.00	10.80
SL42326B-R12KHF	0.12	0.20	76.00	74.00	68.00	61.00	10.90
SL42326A-R15KHF	0.15	0.22	63.00	58.00	54.00	58.00	10.80
SL42326B-R15KHF	0.15	0.20	63.00	58.00	54.00	61.00	10.90
SL42326A-R18KHF	0.18	0.22	51.00	48.00	42.00	58.00	10.80
SL42326B-R18KHF	0.18	0.20	51.00	48.00	42.00	61.00	10.90

3. Mechanical Dimension(Unit:mm):

A Max.	B Max.	C Max.	D Max.	E Nom.	F Nom.	G Nom.
6.20	9.45	8.10	see table	5.40	2.20	2.50



Note:

1>.Open Circuit Inductance (OCL) test condition:100KHz,0.1Vrms,0Adc ,at 25 ℃.

2>.Full Load Inductance (FLL) Test condition:100KHz,0.1Vrms ,Isat ;(Ta=25 ℃).

3>.Isat[1], Isat[2] & Isat[3] : DC current that will cause inductance to drops approximately by 20% ;

4>. Irms: DC current for an approximate temperature rise of 40℃ without core loss,.Derating is necessary for AC currents. PCB pad layout,trace thickness and width,air-flow and proximity of other heat generating components will affect the temperature rise. It is recommended the part temperature not exceed 130°C under worst case operating conditions verified in the end application.

5>.The nominal DCR is measured from point "a" to point"b",as shown above on the mechanical drawing.

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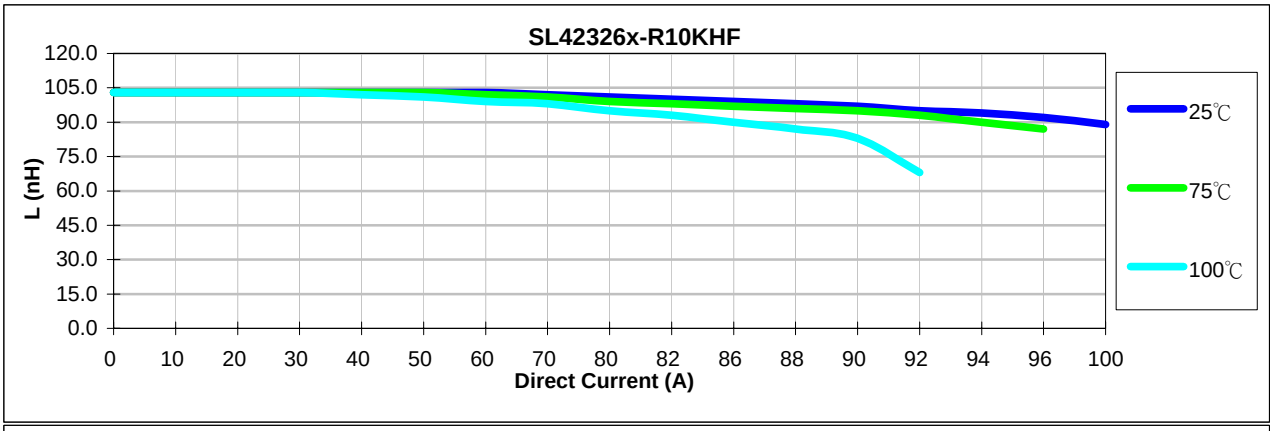
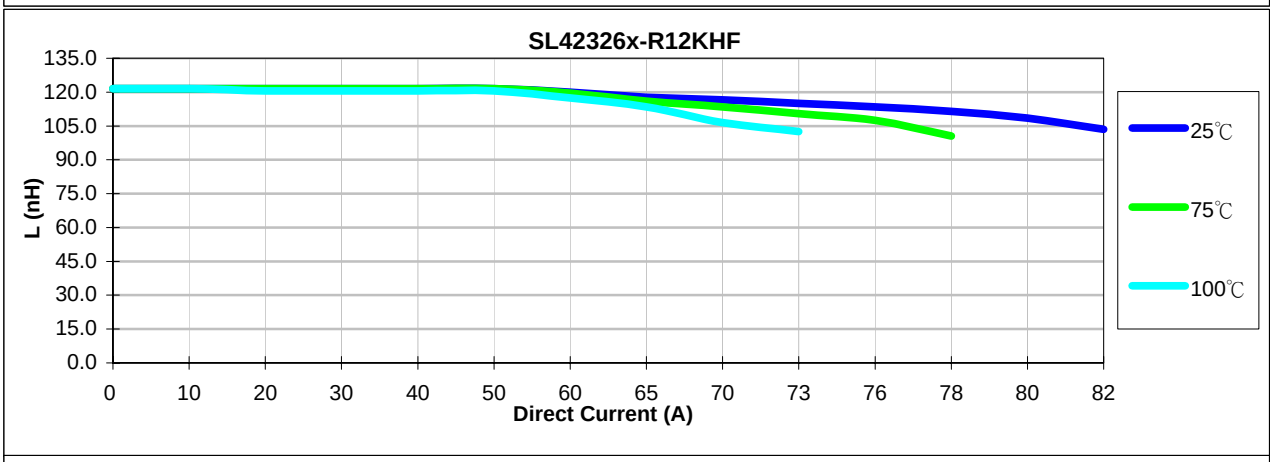
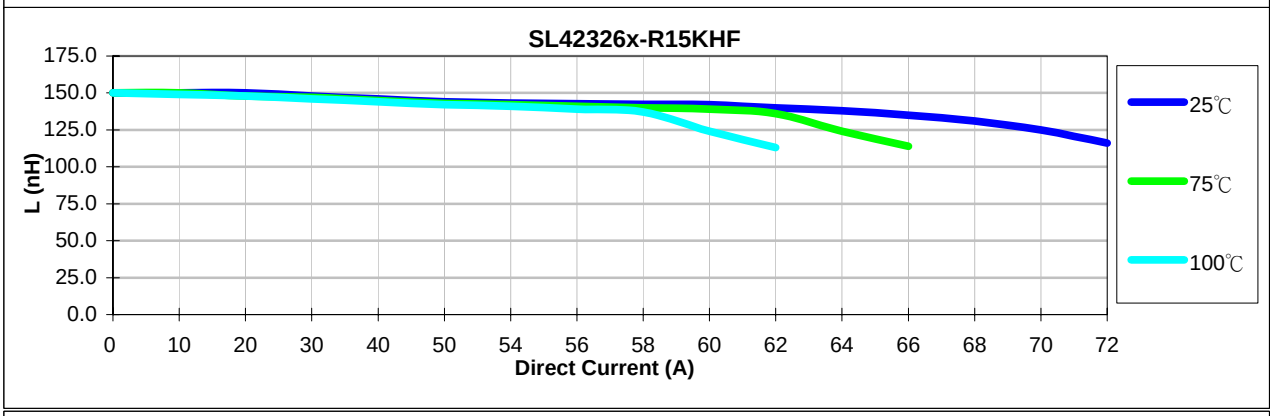
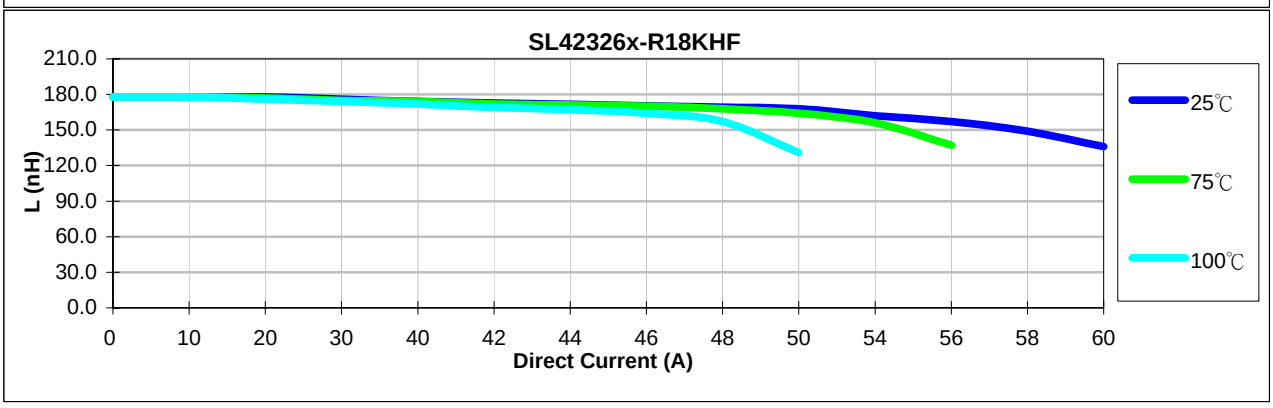
Inductance vs. Current









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