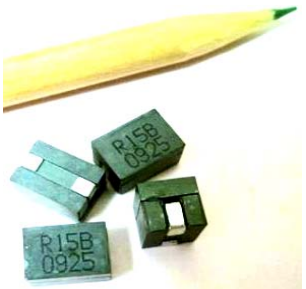
# SL4330 Series

## 1. Features:

· Ferrite based SMD Inductor with lower core loss.
· Inductance Range:150nH to 510nH. Custom values are welcomed.
· High current output chokes, upto 72 Amp with approx. 20% roll off.
· Low Profile 7.5mm Max. height .
· Foot Print 11.0 x 7.2 mm Max.
· Ideal for Buck Converter, VRM & High Density Board Design.
· Operating up to 1 MHz application.
· Operating Temperature Range -55°C to + 130°C , RoHs & HF compliance .
· T & R Qtys: 500 pcs , 13" Reel ;

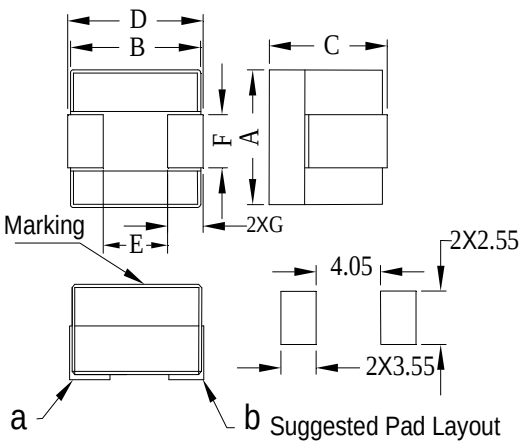



## 2. Electrical Characteristic of SL4330 Series:

| Part Number | Inductance (uH) 10% or 15% | DCR (mΩ) ±5.0% | Isat[1] (A) @25℃ | Isat[2] (A) @45℃ | Isat[3] (A) @100℃ | Isat[4] (A) @125℃ | Irms (A) @25℃ |
|---|---|---|---|---|---|---|---|
| SL4330A-R15KHF | 0.15 , 10% | 0.245 | 72.00 | 65.00 | 61.00 | 58.00 | 59.00 |
| SL4330A-R18KHF | 0.18 , 10% | 0.245 | 63.00 | 61.00 | 54.00 | 50.00 | 59.00 |
| SL4330A-R21KHF | 0.21 ,10% | 0.245 | 51.00 | 48.00 | 44.00 | 37.00 | 59.00 |
| SL4330A-R23KHF | 0.23 ,10% | 0.245 | 50.00 | 47.00 | 43.00 | 36.00 | 59.00 |
| SL4330A-R30KHF | 0.30 , 10% | 0.245 | 36.00 | 34.00 | 29.00 | 28.00 | 59.00 |
| SL4330A-R30LHF | 0.30 , 15% | 0.245 | 36.00 | 34.00 | 29.00 | 28.00 | 59.00 |
| SL4330A-R40LHF | 0.40 , 15% | 0.245 | 26.00 | 25.00 | 23.00 | 20.00 | 59.00 |
| SL4330A-R51LHF | 0.51 , 15% | 0.245 | 19.00 | 18.00 | 17.00 | 16.00 | 59.00 |
| SL4330B-R15KHF | 0.15 , 10% | 0.290 | 72.00 | 65.00 | 61.00 | 58.00 | 55.00 |
| SL4330B-R18KHF | 0.18 , 10% | 0.290 | 63.00 | 61.00 | 54.00 | 50.00 | 55.00 |
| SL4330B-R21KHF | 0.21 ,10% | 0.290 | 51.00 | 48.00 | 44.00 | 37.00 | 55.00 |
| SL4330B-R23KHF | 0.23 ,10% | 0.290 | 50.00 | 47.00 | 43.00 | 36.00 | 55.00 |
| SL4330B-R30KHF | 0.30 , 10% | 0.290 | 36.00 | 34.00 | 29.00 | 28.00 | 55.00 |
| SL4330B-R30LHF | 0.30 , 15% | 0.290 | 36.00 | 34.00 | 29.00 | 28.00 | 55.00 |
| SL4330B-R40LHF | 0.40 , 15% | 0.290 | 26.00 | 25.00 | 23.00 | 20.00 | 55.00 |
| SL4330B-R51LHF | 0.51 , 15% | 0.290 | 19.00 | 18.00 | 17.00 | 16.00 | 55.00 |

## 3. Mechanical Dimension(Unit:mm):

| A Max. | B Max. | C Max. | D Max. | E Nom. | F Nom. | G Nom. |
|---|---|---|---|---|---|---|
| 7.20 | 10.40 | 7.50 | 11.00 | 5.60 | 2.05 | 2.50 |




**Note:**

1>.Open Circuit Inductance (OCL) test condition:100KHz,0.1Vrms,0Adc ,at 25℃.

2>.Full Load Inductance (FLL) Test condition:100KHz,0.1Vrms ,Isat;(Ta=25℃).

3>.Isat[1],Isat[2],Isat[3] & Isat[4]: DC current that will cause inductance to drop approximately by 20%.(Ta=25℃).

4>. Irms: DC current for an approximate temperature rise of 40℃ without core loss,.Derating is necessary for AC currents. PCB pad layout, trace thickness and width, air-flow and proximity of other heat generating components will affect the temperature rise. It is recommended the part temperature not exceed 130°C under worst case operating conditions verified in the end application.

5>.The nominal DCR is measured from point "a" to point"b",as shown above on the mechanical drawing.

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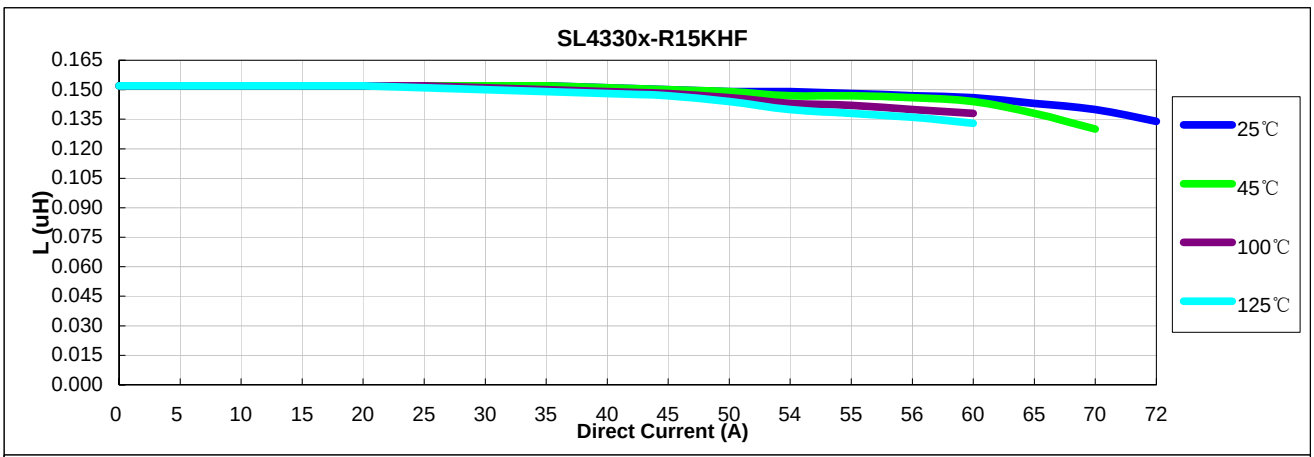
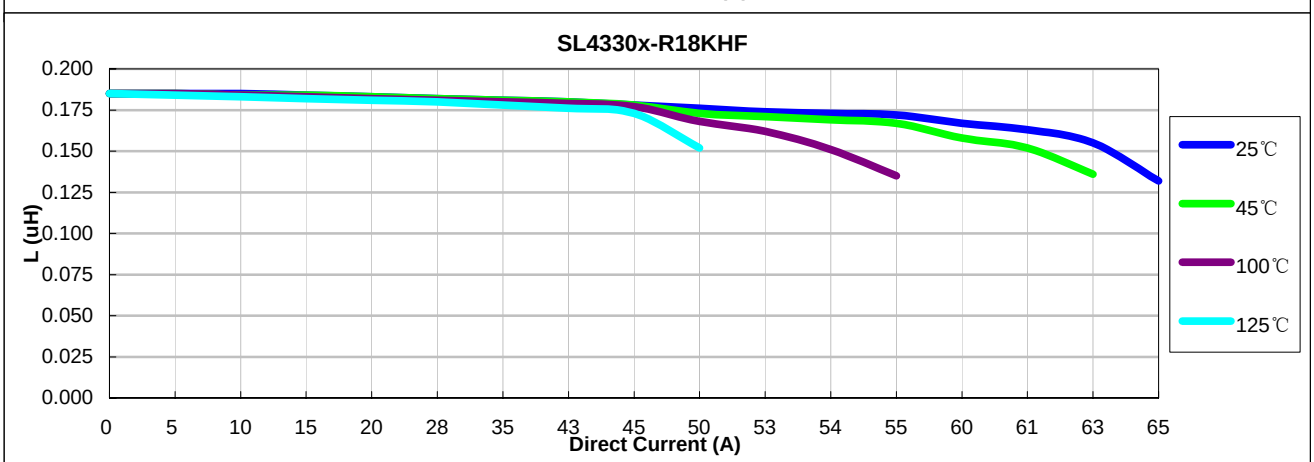
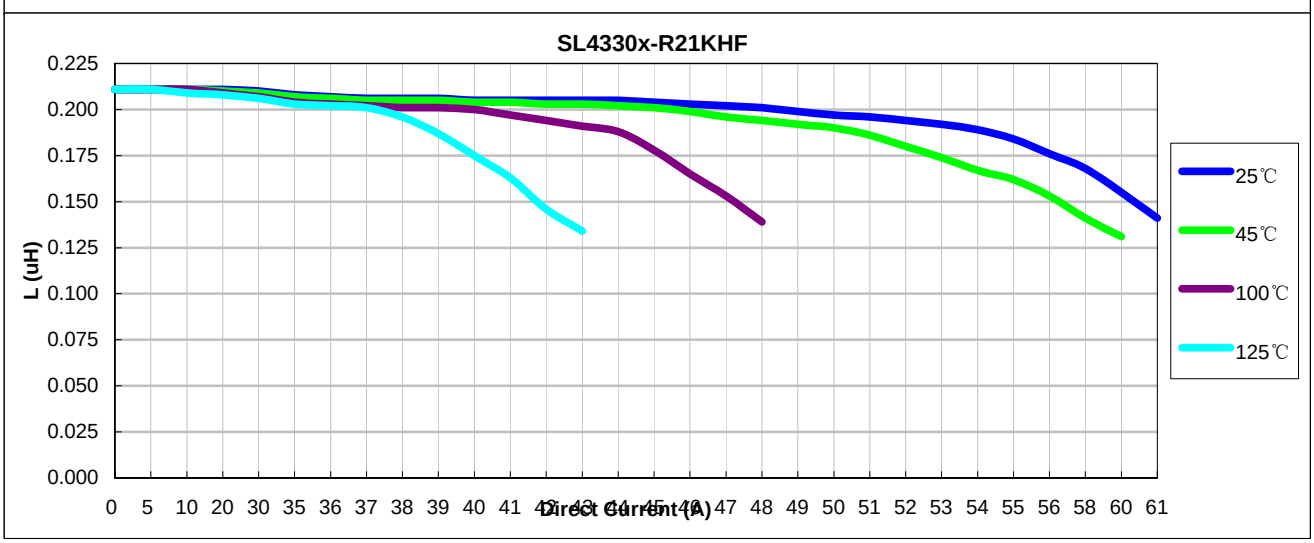
**4. Inductance characteristics(Inductance vs. Current):**



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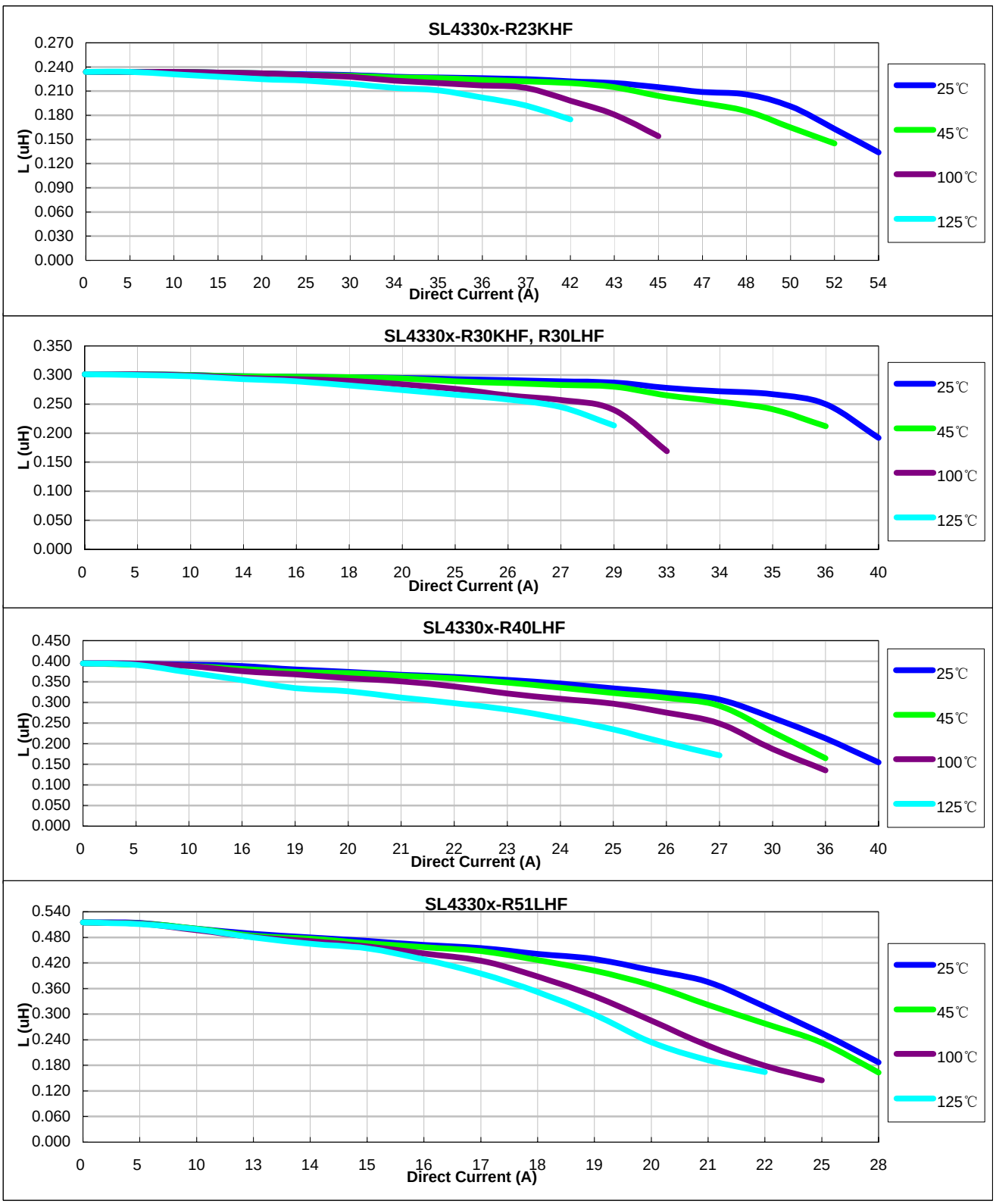
**Inductance vs. Current**

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